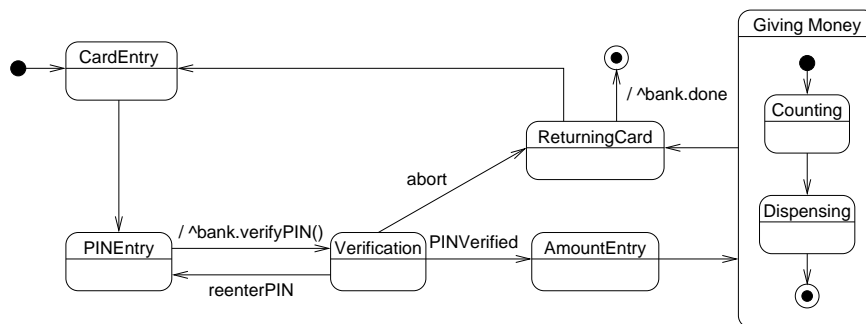
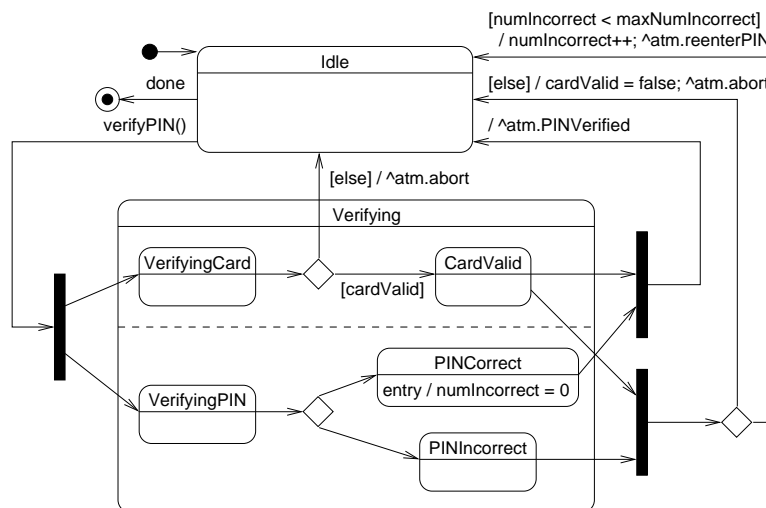


(a) Class diagram



(d) State machine diagram for class ATM



(e) State machine diagram for class Bank

Fig. 1. UML model of an ATM

This example is taken from the following article:
 Model Checking and Code Generation for UML State Machines and Collaborations.
 Alexander Knapp and Stephan Merz.
 In Dominik Haneberg, Gerhard Schellhorn, and Wolfgang Reif, editors,
 Proc. 5th Wsh. Tools for System Design and Verification, pages 59-64.
 Technical Report 2002-11, Institut für Informatik, Universität Augsburg, 2002.